



**Shin'ichiro Kimura**

**Shin'ichiro Kimura** received the B.S. and M.S. degrees in materials science from Tohoku University, Sendai, Japan, in 1978 and 1980, respectively, and the Ph.D. degree from the University of Tokyo, Japan, in 1989.

In 1980, he joined the Central Research Laboratory, Hitachi Ltd., Tokyo, Japan, where he was first involved in the research of high dielectric constant insulators. He then worked on the plasma oxidation of silicon. From 1986 to 1988, he was working on the process design and device characterization for MOS dynamic memories for 16 Mbit. During 1988 to 1989, he was a Visiting Research Associate at the University of Warwick, Coventry, U. K. He is now a senior chief researcher.

Dr. Kimura has served as a member of the IEDM (International Electron Device Meeting) 1997-1998 CMOS Devices and Reliability subcommittees, program committees of 1995-1998 SSDM (Solid State Devices and Materials Conference), and steering committee member in 2003, and program chair of 2005 and conference chair of 2007 Symposium on VLSI Technology. Since March 2002 through 2007, he is one of the editors of IEEE Transactions on Electron Devices. From 2008, he has been vice-chair of IEEE EDS Tokyo Chapter.