

SIMON DELEONIBUS



SIMON DELEONIBUS received the MSc in Physics and PhD degrees in Applied Physics from Paris University in 1979 and 1982 respectively. He joined Thomson Semiconducteurs (Grenoble) in 1981. He joined LETI(CEA) in 1986 as expert on CMOS and Flash memories integration. From 1996 to 2008, he directed the Electronic Nanodevices Laboratory(60 researchers), leading several industrial, National and European projects. He is now Chief Scientist of LETI-Silicon Technologies (850 researchers). Published more than 500 papers (conferences and journals ; 55 invited) and owns 30 patents; Editor of 1 book (WSPC). Author of 8 book chapters. Editor of IEEE Transactions on Electron Devices. Member of Conferences Program Committees such as IEDM, VLSI Tech Symposium, ESSDERC, ECS Symposia, ITRS. Member of Board of Directors of Nanosciences Foundation and Panelist European Research Council. Co-recipient of 7 Best Papers Awards. IEEE Fellow ; French CEA Research Director; IEEE Distinguished Lecturer; “Chevalier de l’Ordre National du Mérite” by French Presidence Decree; Recipient of the “2005 Grand Prix de l’Académie des Technologies”.